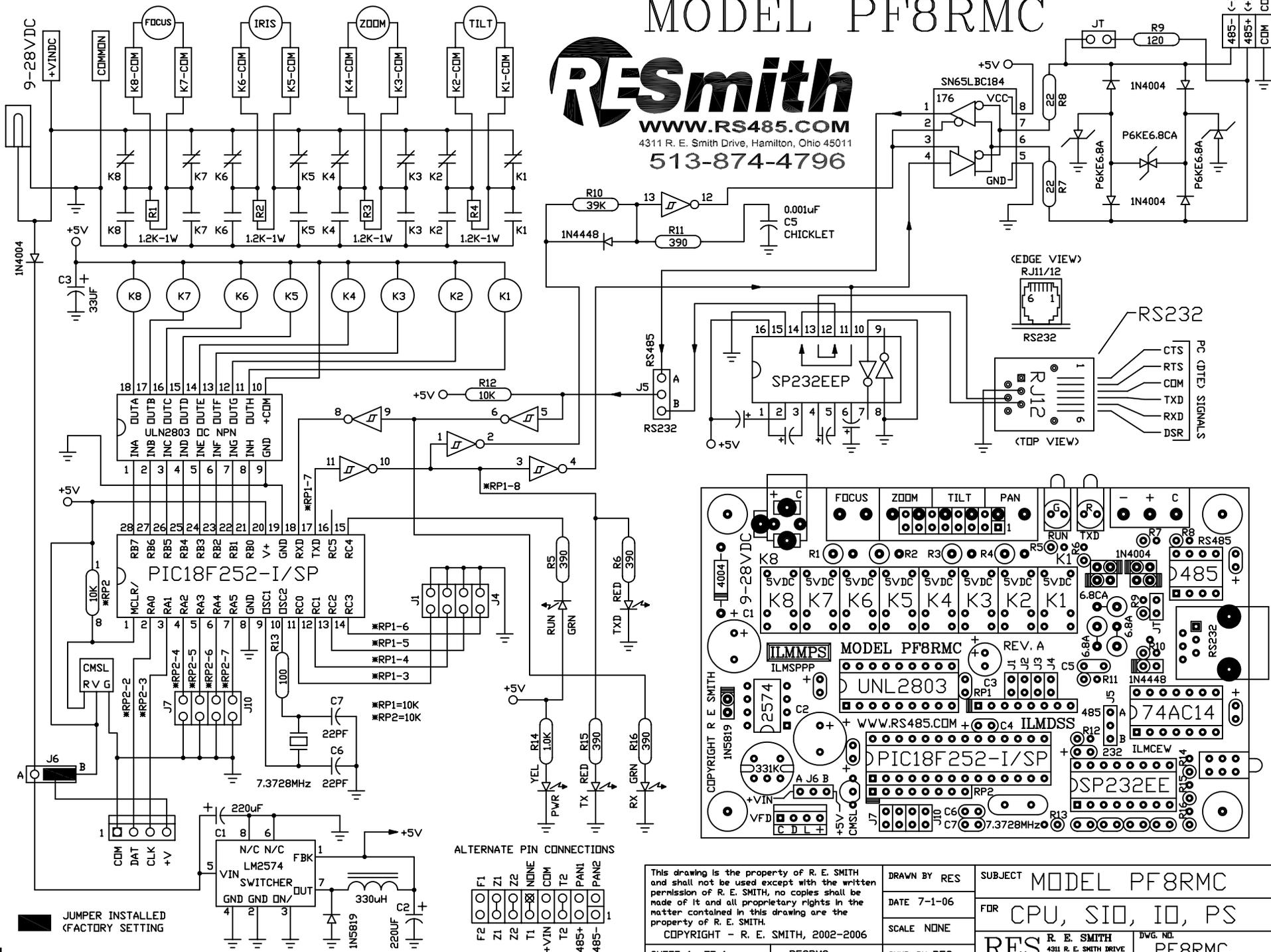


# MODEL PF8RMC

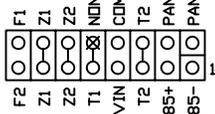


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JUMPER INSTALLED  
(FACTORY SETTING)

### ALTERNATE PIN CONNECTIONS



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DATE 7-1-06	FOR CPU, SIO, IO, PS	
SCALE NONE	RES R. E. SMITH	DWG. NO. PF8RMC
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